

10

Fig. 1

AUS920010940US1 2/4

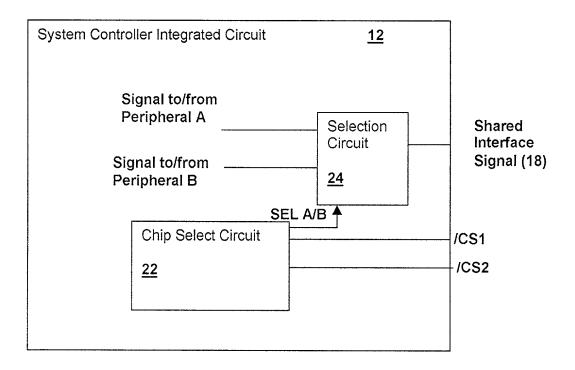


Fig. 2

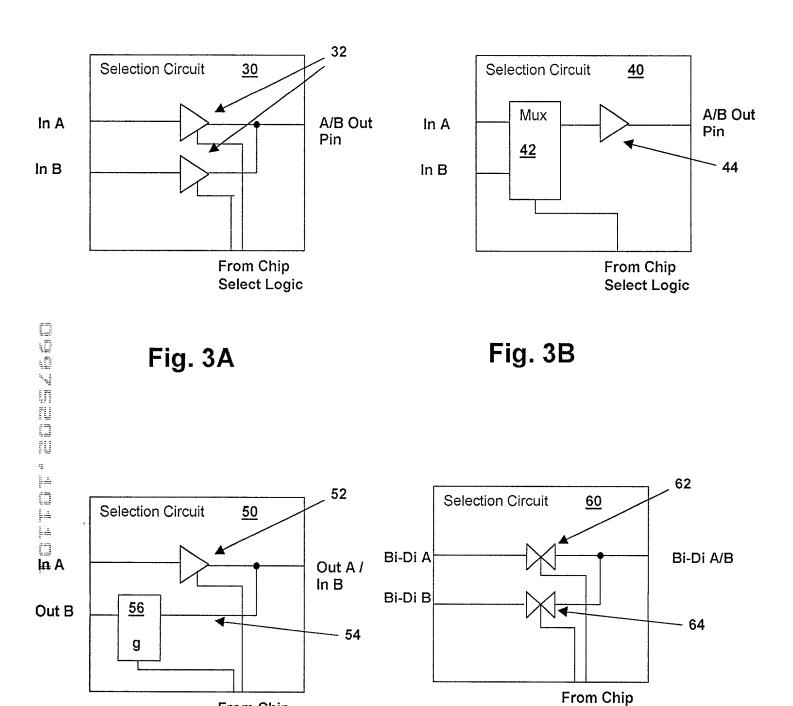


Fig. 3C

From Chip

Select Logic

Fig. 3D

Select Logic

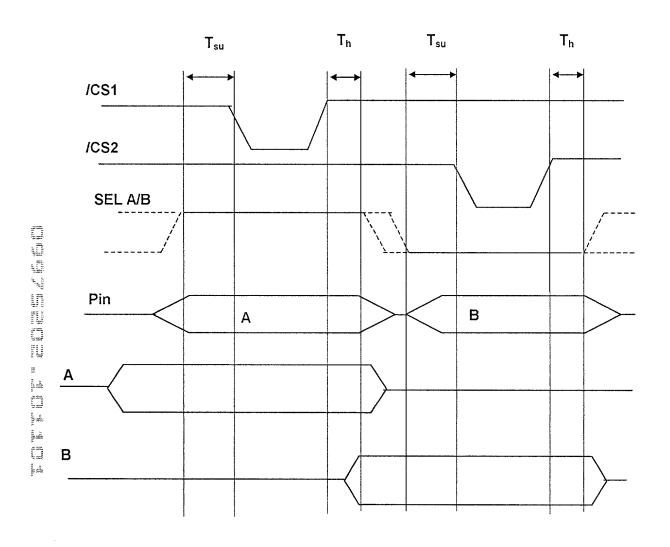


Fig. 4